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Boolean Circuit Rewiring Bridging Logical And Physical Designs

**Tak-Kei Lam, Wai-Chung Tang, Xing
Wei, Yi Diao, David Yu-Liang Wu**

Boolean Circuit Rewiring Bridging Logical And Physical Designs:

Boolean Circuit Rewiring Tak-Kei Lam,Wai-Chung Tang,Xing Wei,Yi Diao,David Yu-Liang Wu,2016-01-06 Demonstrates techniques which will allow rewiring rates of over 95% enabling adoption of deep sub micron chips for industrial applications Logic synthesis is an essential part of the modern digital IC design process in semiconductor industry This book discusses a logic synthesis technique called rewiring and its latest technical advancement in term of rewirability Rewiring technique has surfaced in academic research since 1993 and there is currently no book available on the market which systematically and comprehensively discusses this rewiring technology The authors cover logic transformation techniques with concentration on rewiring For many decades the effect of wiring on logic structures has been ignored due to an ideal view of wires and their negligible role in the circuit performance However in today's semiconductor technology wiring is the major player in circuit performance degeneration and logic synthesis engines can be improved to deal with this through wire based transformations This book introduces the automatic test pattern generation ATPG based rewiring techniques which are recently active in the realm of logic synthesis verification of VLSI SOC designs Unique comprehensive coverage of semiconductor rewiring techniques written by leading researchers in the field Provides complete coverage of rewiring from an introductory to intermediate level Rewiring is explained as a flexible technique for Boolean logic synthesis introducing the concept of Boolean circuit transformation and testing with examples Readers can directly apply the described techniques to real world VLSI design issues Focuses on the automatic test pattern generation ATPG based rewiring methods although some non ATPG based rewiring methods such as graph based alternative wiring GBAW and set of pairs of functions to be distinguished SPFD based rewiring are also discussed A valuable resource for researchers and postgraduate students in VLSI and SoC design as well as digital design engineers EDA software developers and design automation experts that specialize in the synthesis and optimization of logical circuits

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designs Unique comprehensive coverage of semiconductor rewiring techniques written by leading researchers in the field Provides complete coverage of rewiring from an introductory to intermediate level Rewiring is explained as a flexible technique for Boolean logic synthesis introducing the concept of Boolean circuit transformation and testing with examples Readers can directly apply the described techniques to real world VLSI design issues Focuses on the automatic test pattern generation ATPG based rewiring methods although some non ATPG based rewiring methods such as graph based alternative wiring GBAW and set of pairs of functions to be distinguished SPFD based rewiring are also discussed A valuable resource for researchers and postgraduate students in VLSI and SoC design as well as digital design engineers EDA software developers and design automation experts that specialize in the synthesis and optimization of logical circuits Production Factor Mathematics Martin Grötschel,Klaus Lucas,Volker Mehrmann,2010-08-05 Mathematics as a production factor or driving force for innovation Those who want to know and understand why mathematics is deeply involved in the design of products the layout of production processes and supply chains will find this book an indispensable and rich source Describing the interplay between mathematical and engineering sciences the book focusses on questions like How can mathematics improve to the improvement of technological processes and products What is happening already Where are the deficits What can we expect for the future 19 articles written by mixed teams of authors of engineering industry and mathematics offer a fascinating insight of the interaction between mathematics and engineering Science Abstracts ,1993 Electrical & Electronics Abstracts ,1997 Advanced Logical Circuit Design Techniques Antonín Svoboda,Donnamaie E. White,1979

Logical Design of Switching Circuits Douglas Lewin,1974 **Introduction to Switching Theory and Logical Design** Fredrick J. Hill,Gerald R. Peterson,1968 **Advanced Logical Circuit Design Techniques** Donnamaie E. White,2000

Reasoning in Boolean Networks Wolfgang Kunz,Dominik Stoffel,1997-06-30 Reasoning in Boolean Networks provides a detailed treatment of recent research advances in algorithmic techniques for logic synthesis test generation and formal verification of digital circuits The book presents the central idea of approaching design automation problems for logic level circuits by specific Boolean reasoning techniques While Boolean reasoning techniques have been a central element of two level circuit theory for many decades Reasoning in Boolean Networks describes a basic reasoning methodology for multi level circuits This leads to a unified view on two level and multi level logic synthesis The presented reasoning techniques are applied to various CAD problems to demonstrate their usefulness for today's industrially relevant problems Reasoning in Boolean Networks provides lucid descriptions of basic algorithmic concepts in automatic test pattern generation logic synthesis and verification and elaborates their intimate relationship to provide further intuition and insight into the subject Numerous examples are provided for ease in understanding the material Reasoning in Boolean Networks is intended for researchers in logic synthesis VLSI testing and formal verification as well as for integrated circuit designers who want to enhance their understanding of basic CAD methodologies Switching Circuits; Theory and Logic Design H. C. Torng,1972

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William E. Wickes,1968

Logic

Synthesis and Optimization Tsutomu Sasao,2012-12-06 Logic Synthesis and Optimization presents up to date research information in a pedagogical form The authors are recognized as the leading experts on the subject The focus of the book is on logic minimization and includes such topics as two level minimization multi level minimization application of binary decision diagrams delay optimization asynchronous circuits spectral method for logic design field programmable gate array FPGA design EXOR logic synthesis and technology mapping Examples and illustrations are included so that each contribution can be read independently Logic Synthesis and Optimization is an indispensable reference for academic researchers as well as professional CAD engineers

Fundamentals of Switching Theory and Logic Design Jaakko Astola, Radomir S.

Stankovic,2006-03-06 Switching theory and logic design provide mathematical foundations and tools for digital system design that is an essential part in the research and development in almost all areas of modern technology The vast complexity of modern digital systems implies that they can only be handled by computer aided design tools that are built on sophisticated mathematical models Fundamentals of Switching Theory and Logic Design is aimed at providing an accessible introduction to these mathematical techniques that underlie the design tools and that are necessary for understanding their capabilities and limitations As is typical to many disciplines a high level of abstraction enables a unified treatment of many methodologies and techniques as well as provides a deep understanding of the subject in general The drawback is that without a hands on touch on the details it is difficult to develop an intuitive understanding of the techniques We try to combine these views by providing hands on examples on the techniques while binding these to the more general theory that is developed in parallel For instance the use of vector spaces and group theory unifies the spectral Fourier like interpretation of polynomial and graphic decision diagrams representations of logic functions as well as provides new methods for optimization of logic functions Consequently Fundamentals of Switching Theory and Logic Design discusses the fundamentals of switching theory and logic design from a slightly alternative point of view and also presents links between switching theory and related areas of signal processing and system theory It also covers the core topics recommended in IEEE ACM curricula for teaching and study in this area Further it contains several elective sections discussing topics for further research work in this area

Fundamentals of Logic Design and Switching Theory Arthur D. Friedman,1986

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