



ANALOG DESIGN AND SIMULATION USING OrCAD® CAPTURE AND PSpice®

Key Features

- Reviewed by Cadence Design Systems, Inc.
- A new chapter on PSpice Advanced Analysis
- Updated to using OrCAD Release 17.2 and its new features
- Includes PSpice Advanced Analysis suite of tools
- Coverage of PSpice extra features

Second Edition

Dennis Fitzpatrick

Analog Design And Simulation Using Orcad Capture And Pspice

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Analog Design And Simulation Using Orcad Capture And Pspice:

Analog Design and Simulation Using OrCAD Capture and PSpice Dennis Fitzpatrick, 2017-12-11 New to this edition Updated to using OrCAD Release 17.2 and its new features Coverage of PSpice extra features PSpice Designer PSpice Designer Plus Modelling Application PSpice Part Search Symbol Viewer PSpice Report Associate PSpice model New delay functions for Behavioural Simulation Models New Models Support for negative values in hysteresis voltage and threshold voltage A new chapter on PSpice Advanced Analysis Analog Design and Simulation Using OrCAD Capture and PSpice Second Edition provides step by step instructions on how to use the Cadence OrCAD family of Electronic Design Automation software for analog design and simulation The book explains how to enter schematics in Capture set up project types project libraries and prepare circuits for PSpice simulation There are chapters on the different analysis types for DC Bias point DC sweep AC frequency sweep Parametric analysis Temperature analysis Performance Analysis Noise analysis Sensitivity and Monte Carlo simulation Subsequent chapters explain how the Stimulus Editor is used to define custom analog and digital signals how the Model Editor is used to view and create new PSpice models and Capture parts and how the Magnetic Parts Editor is used to design transformers and inductors Other chapters include Analog Behavioral models Test Benches as well as how to create hierarchical designs The book includes the latest features in the OrCAD 17.2 release and there are exercises with step by step instructions at the end of each chapter that enables the reader to progress based upon their experience and knowledge gained from previous chapters The author worked for Cadence for over eight years and supported and delivered OrCAD PSpice training courses all over Europe This book has been endorsed by Cadence In addition there are new chapters on the PSpice Advanced Analysis suite of tools Sensitivity Analysis Optimizer Monte Carlo and Smoke Analysis The chapters show how circuit performance can effectively be maximised and optimised for variations in component tolerances temperature effects manufacturing yields and component stress Provides both a comprehensive user guide and a detailed overview of simulation using OrCAD Capture and PSpice Includes worked and ready to try sample designs and a wide range of to do exercises Covers Capture and PSpice together

Analog Design and Simulation using OrCAD Capture and PSpice Dennis Fitzpatrick, 2011-09-28 Analog Design and Simulation using OrCAD Capture and PSpice provides step by step instructions on how to use the Cadence OrCAD family of Electronic Design Automation software for analog design and simulation Organized into 22 chapters each with exercises at the end it explains how to start Capture and set up the project type and libraries for PSpice simulation It also covers the use of AC analysis to calculate the frequency and phase response of a circuit and DC analysis to calculate the circuits bias point over a range of values The book describes a parametric sweep which involves sweeping a parameter through a range of values along with the use of Stimulus Editor to define transient analog and digital sources It also examines the failure of simulations due to circuit errors and missing or incorrect parameters and discusses the use of Monte Carlo analysis to estimate the response of a circuit when device model parameters are randomly varied

between specified tolerance limits according to a specified statistical distribution Other chapters focus on the use of worst case analysis to identify the most critical components that will affect circuit performance how to add and create PSpice models and how the frequency related signal and dispersion losses of transmission lines affect the signal integrity of high speed signals via the transmission lines Practitioners researchers and those interested in using the Cadence OrCAD professional simulation software to design and analyze electronic circuits will find the information methods compounds and experiments described in this book extremely useful Provides both a comprehensive user guide and a detailed overview of simulation Each chapter has worked and ready to try sample designs and provides a wide range of to do exercises Core skills are developed using a running case study circuit Covers Capture and PSpice together for the first time SPICE and LTspice for Power Electronics and Electric Power Muhammad H. Rashid, 2024-11-13 Power electronics can be a difficult course for students to understand and for professional professors to teach simplifying the process for both LTspice for power electronics and electrical power edition illustrates methods of integrating industry standard LTspice software for design verification and as a theoretical laboratory bench Helpful LTspice software and Program Files Available for Download Based on the author Muhammad H Rashid s considerable experience merging design content and SPICE into a power electronics course this vastly improved and updated edition focuses on helping readers integrate the LTspice simulator with a minimum amount of time and effort Giving users a better understanding of the operation of a power electronic circuit the author explores the transient behavior of current and voltage waveforms for every circuit element at every stage The book also includes examples of common types of power converters as well as circuits with linear and nonlinear inductors New in this edition Changes to run on OrCAD SPICE or LTspice IV or higher Students learning outcomes SLOs listed at the start of each chapter Abstracts of chapters List the input side and output side performance parameters of the converters The characteristics of power semiconductors diodes BJTs MOSFETs and IGBTs Generating PWM and sinusoidal PWM gating signals Evaluating the power efficiency of converters Monte Carlo analysis of converters Worst case analysis of converters Nonlinear transformer model Evaluate user defined electrical quantities MEASURE This book demonstrates techniques for executing power conversion and ensuring the quality of output waveform rather than the accurate modeling of power semiconductor devices This approach benefits students enabling them to compare classroom results obtained with simple switch models of devices VLSI Design and Test Brajesh Kumar Kaushik, Sudeb Dasgupta, Virendra Singh, 2017-12-21 This book constitutes the refereed proceedings of the 21st International Symposium on VLSI Design and Test VDAT 2017 held in Roorkee India in June July 2017 The 48 full papers presented together with 27 short papers were carefully reviewed and selected from 246 submissions The papers were organized in topical sections named digital design analog mixed signal VLSI testing devices and technology VLSI architectures emerging technologies and memory system design low power design and test RF circuits architecture and CAD and design verification **Noise Coupling in System-on-Chip** Thomas

Noulis,2018-01-09 Noise Coupling is the root cause of the majority of Systems on Chip SoC product fails The book discusses a breakthrough substrate coupling analysis flow and modelling toolset addressing the needs of the design community The flow provides capability to analyze noise components propagating through the substrate the parasitic interconnects and the package Using this book the reader can analyze and avoid complex noise coupling that degrades RF and mixed signal design performance while reducing the need for conservative design practices With chapters written by leading international experts in the field novel methodologies are provided to identify noise coupling in silicon It additionally features case studies that can be found in any modern CMOS SoC product for mobile communications automotive applications and readout front ends

Nanoelectronic Mixed-Signal System Design Saraju Mohanty,2015-02-20 Covering both the classical and emerging nanoelectronic technologies being used in mixed signal design this book addresses digital analog and memory components Winner of the Association of American Publishers 2016 PROSE Award in the Textbook Physical Sciences Mathematics category Nanoelectronic Mixed Signal System Design offers professionals and students a unified perspective on the science engineering and technology behind nanoelectronics system design Written by the director of the NanoSystem Design Laboratory at the University of North Texas this comprehensive guide provides a large scale picture of the design and manufacturing aspects of nanoelectronic based systems It features dual coverage of mixed signal circuit and system design rather than just digital or analog only Key topics such as process variations power dissipation and security aspects of electronic system design are discussed Top down analysis of all stages from design to manufacturing Coverage of current and developing nanoelectronic technologies not just nano CMOS Describes the basics of nanoelectronic technology and the structure of popular electronic systems Reveals the techniques required for design excellence and manufacturability

Introduction to PSpice Using OrCAD for Circuits and Electronics M. H. Rashid,2004 This book uses a top down approach to introduce readers to the SPICE simulator It begins by describing techniques for simulating circuits then presents the various SPICE and OrCAD commands and their applications to electrical and electronic circuits Lavishly illustrated this new edition includes even more hands on exercises suggestions sample problems and circuit models of actual devices It is an ideal supplement for courses in electric or electronic circuitry and is also a solid professional reference BOOK JACKET Title Summary field provided by Blackwell North America Inc All Rights Reserved

Switch-Mode Power Supply Simulation: Designing with SPICE 3 Steven Sandler,2005-12-02 CD ROM contains SPICE3 and ISPIICE simulation models and examples from the book allowing easy customization

Functional Reverse Engineering of Strategic and Non-Strategic Machine Tools Wasim Ahmed Khan,Khalid Rahman,Ghulam Hussain,Ghulam Abbas,2021-06-20 This book describes capacity building in strategic and non strategic machine tool technology It includes machine building in sectors such as machine tools automobiles home appliances energy and biomedical engineering along with case studies The book offers guidelines for capacity building in academia covering how to promote enterprises of functional reverse engineering

enterprises It also discusses machine tool development engineering design prototyping of strategic and non strategies machine tools as well as presenting communication strategies and IoT along with case studies Professionals from the CNC Computer Numeric Control machine tools industry industrial and manufacturing engineers and students and faculty in engineering disciplines will find interest in this book

Switch-Mode Power Supply Simulation: Designing with SPICE 3 : Designing with SPICE 3 Steven Sandler,2005-11-11 A master class in power supply design through circuit simulation This book CD ROM package covers every essential aspect of power supply design simulation and fully explains the fundamentals of SPICE 3 simulation techniques CD ROM contains SPICE3 and ISPICE simulation models and examples from the book allowing easy customization

Complete PCB Design Using OrCad Capture and Layout Kraig Mitzner,2011-04-01 Complete PCB Design Using OrCad Capture and Layout provides instruction on how to use the OrCAD design suite to design and manufacture printed circuit boards The book is written for both students and practicing engineers who need a quick tutorial on how to use the software and who need in depth knowledge of the capabilities and limitations of the software package There are two goals the book aims to reach The primary goal is to show the reader how to design a PCB using OrCAD Capture and OrCAD Layout Capture is used to build the schematic diagram of the circuit and Layout is used to design the circuit board so that it can be manufactured The secondary goal is to show the reader how to add PSpice simulation capabilities to the design and how to develop custom schematic parts footprints and PSpice models Often times separate designs are produced for documentation simulation and board fabrication This book shows how to perform all three functions from the same schematic design This approach saves time and money and ensures continuity between the design and the manufactured product Information is presented in the exact order a circuit and PCB are designed Straightforward realistic examples present the how and why the designs work providing a comprehensive toolset for understanding the OrCAD software Introduction to the IPC JEDEC and IEEE standards relating to PCB design Full color interior and extensive illustrations allow readers to learn features of the product in the most realistic manner possible

Electronic Design ,2001
EDN ,2000

Lego Mindstorms Mechatronics Don Wilcher,2003 Focuses on hot technology topics electronics embedded systems object oriented technology software development and robotics This book also includes projects for each concept including a LEGO camera for the remote control vision chapter an interface for a robotic warning system and a tele operated robot

Proceedings American Society for Engineering Education. Conference,1993

Electronic Business ,2006 The management magazine for the electronics industry

The Software Encyclopedia ,1997

Computer Design ,1986

OrCAD PSpice for Windows Roy W. Goody,2001 A circuit simulation book that addresses PSpice for Windows Using a careful step by step process this book takes readers from a simple dc circuit with its customary current and voltage measurements through a damped resonant circuit requiring rise time to characteristics to a complex analog digital circuit that demands sophisticated timing and frequency measurements Multi level activities examples and exercises allow users to

draw circuits directly on the screen exactly as they would to create initial designs It also enables them to analyze the circuits using PSpice and shows the circuit displayed at a level of detail unavailable through conventional techniques This book also includes instruction in advanced techniques such as Monte Carlo and Performance For instruction in DC AC and devices operational amplifiers digital analog and filter design n *ASIC & EDA* ,1994

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