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October 24-25, 2023

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October 24 | 11:50

Advanced Lithography 2015 Symposium Cd

**Wynand Lambrechts, Saurabh
Sinha, Jassem Ahmed Abdallah, Jaco
Prinsloo**

Advanced Lithography 2015 Symposium Cd:

Microlithography Bruce W. Smith,Kazuaki Suzuki,2020-05-01 The completely revised Third Edition to the bestselling Microlithography Science and Technology provides a balanced treatment of theoretical and operational considerations from fundamental principles to advanced topics of nanoscale lithography The book is divided into chapters covering all important aspects related to the imaging materials and processes that have been necessary to drive semiconductor lithography toward nanometer scale generations Renowned experts from the world s leading academic and industrial organizations have provided in depth coverage of the technologies involved in optical deep ultraviolet DUV immersion multiple patterning extreme ultraviolet EUV maskless nanoimprint and directed self assembly lithography together with comprehensive descriptions of the advanced materials and processes involved New in the Third Edition In addition to the full revision of existing chapters this new Third Edition features coverage of the technologies that have emerged over the past several years including multiple patterning lithography design for manufacturing design process technology co optimization maskless lithography and directed self assembly New advances in lithography modeling are covered as well as fully updated information detailing the new technologies systems materials and processes for optical UV DUV immersion and EUV lithography The Third Edition of Microlithography Science and Technology authoritatively covers the science and engineering involved in the latest generations of microlithography and looks ahead to the future systems and technologies that will bring the next generations to fruition Loaded with illustrations equations tables and time saving references to the most current technology this book is the most comprehensive and reliable source for anyone from student to seasoned professional looking to better understand the complex world of microlithography science and technology

Extending Moore's Law through Advanced Semiconductor Design and Processing Techniques Wynand Lambrechts,Saurabh Sinha,Jassem Ahmed Abdallah,Jaco Prinsloo,2018-09-13 This book provides a methodological understanding of the theoretical and technical limitations to the longevity of Moore s law The book presents research on factors that have significant impact on the future of Moore s law and those factors believed to sustain the trend of the last five decades Research findings show that boundaries of Moore s law primarily include physical restrictions of scaling electronic components to levels beyond that of ordinary manufacturing principles and approaching the bounds of physics The research presented in this book provides essential background and knowledge to grasp the following principles Traditional and modern photolithography the primary limiting factor of Moore s law Innovations in semiconductor manufacturing that makes current generation CMOS processing possible Multi disciplinary technologies that could drive Moore s law forward significantly Design principles for microelectronic circuits and components that take advantage of technology miniaturization The semiconductor industry economic market trends and technical driving factors The complexity and cost associated with technology scaling have compelled researchers in the disciplines of engineering and physics to optimize previous generation

nodes to improve system on chip performance This is especially relevant to participate in the increased attractiveness of the Internet of Things IoT This book additionally provides scholarly and practical examples of principles in microelectronic circuit design and layout to mitigate technology limits of previous generation nodes Readers are encouraged to intellectually apply the knowledge derived from this book to further research and innovation in prolonging Moore's law and associated principles

Proceedings of 4th International Conference on Intelligent Systems Design and Engineering Applications

Yongsheng Ma,2026-01-01 This volume of proceedings presents selected and peer reviewed papers from 4th International Conference on Intelligent Systems Design and Engineering Applications which took place in Seoul South Korea during April 19 21 2025 The research topics covered in this book include Machine learning and deep learning Cloud computing and cloud storage Human machine intelligent interaction system Intelligent control and automatic detection technology Intelligent manufacturing and design Computer Aided Design and Manufacturing Product Management and Manufacturing Information System The book serves as a valuable reference for researchers scholars and engineers working in this field *Machine Learning in VLSI Computer-Aided Design* Ibrahim (Abe) M. Elfadel,Duane S. Boning,Xin Li,2019-03-15 This book provides readers with an up to date account of the use of machine learning frameworks methodologies algorithms and techniques in the context of computer aided design CAD for very large scale integrated circuits VLSI Coverage includes the various machine learning methods used in lithography physical design yield prediction post silicon performance analysis reliability and failure analysis power and thermal analysis analog design logic synthesis verification and neuromorphic design Provides up to date information on machine learning in VLSI CAD for device modeling layout verifications yield prediction post silicon validation and reliability Discusses the use of machine learning techniques in the context of analog and digital synthesis Demonstrates how to formulate VLSI CAD objectives as machine learning problems and provides a comprehensive treatment of their efficient solutions Discusses the tradeoff between the cost of collecting data and prediction accuracy and provides a methodology for using prior data to reduce cost of data collection in the design testing and validation of both analog and digital VLSI designs From the Foreword As the semiconductor industry embraces the rising swell of cognitive systems and edge intelligence this book could serve as a harbinger and example of the osmosis that will exist between our cognitive structures and methods on the one hand and the hardware architectures and technologies that will support them on the other As we transition from the computing era to the cognitive one it behooves us to remember the success story of VLSI CAD and to earnestly seek the help of the invisible hand so that our future cognitive systems are used to design more powerful cognitive systems This book is very much aligned with this on going transition from computing to cognition and it is with deep pleasure that I recommend it to all those who are actively engaged in this exciting transformation Dr Ruchir Puri IBM Fellow IBM Watson CTO Chief Architect IBM T J Watson Research Center **Metrology and Diagnostic Techniques for Nanoelectronics** Zhiyong Ma,David G. Seiler,2017-03-27 Nanoelectronics is changing the way the world communicates

and is transforming our daily lives Continuing Moore's law and miniaturization of low power semiconductor chips with ever increasing functionality have been relentlessly driving R&D of new devices materials and process capabilities to meet performance power and cost requirements This book covers up to date advances in research and industry practices in nanometrology critical for continuing technology scaling and product innovation It holistically approaches the subject matter and addresses emerging and important topics in semiconductor R&D and manufacturing It is a complete guide for metrology and diagnostic techniques essential for process technology electronics packaging and product development and debugging a unique approach compared to other books The authors are from academia government labs and industry and have vast experience and expertise in the topics presented The book is intended for all those involved in IC manufacturing and nanoelectronics and for those studying nanoelectronics process and assembly technologies or working in device testing characterization and diagnostic techniques

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