

Model Question Paper-I/II with effect from 2022-23 (CBCS Scheme)

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First/Second Semester B.E. Degree Examination
Introduction to Electronics Engineering

TIME: 03 Hours

Max. Marks: 100

Note: Q1. Answer any **FTVE** full questions, choosing at least **ONE** question from each **MODULE**.

Module - I				*Bloom's Taxonomy Level	Marks					
Q. 01	a	What is a regulated power supply? With neat block diagram Summarize the working of DC power supply. Also mention the principal components used in each block.					L.2	6M		
	b	Discuss the need of filter circuit. With circuit diagram and waveforms brief out the operation of smoothing filter for full wave rectifiers.					L.2	7M		
	c	With neat diagram Summarize working principle of the voltage divider bias CE amplifier with feedback.					L.2	7M		
OR										
Q. 02	a	A 5V zener diode has a maximum rated power dissipation of 500 mW. If the diode is to be used in a simple regulator circuit to supply a regulated 5V to a load having a resistance of 500 Ω , determine a suitable value of series resistor for operation in conjunction with a supply of 9V.					L.3	7M		
	b	What is voltage multiplier and mention its applications? With circuit diagram brief out the operation of voltage Tripler circuit.					L.2	7M		
	c	Illustrate how BJT is used as a switch.					L.4	6M		
Module-2										
Q. 03	a	Sketch the circuits of each of the following based on use of Operational Amplifier a) Differentiator, b) Integrator .					L.1	6M		
	b	Write a note on Ideal characteristics of Op-Amp					L.1	7M		
	c	Explain the operation of Single stage Astable Oscillator with its circuit diagram.					L.2	7M		
OR										
Q. 04	a	Mention the condition of sustained oscillations. Determine the frequency of oscillations of a three stage ladder network in which $C=10\text{nF}$ and $R=10\text{K}\Omega$.					L.2	6M		
	b	With a neat circuit diagram and Waveforms, describe the operation of Crystal controlled Oscillator.					L.2	7M		
	c	With a neat circuit diagram explain single stage Multivibrators.					L.2	7M		
Module-3										
Q. 05	a	With the help of truth table explain the operation of Full Adder with its circuit diagram and reduce the expression for Sum and carry.					L.2	7M		
	b	Mention the different theorems and Postulates of Boolean Algebra and Prove each of them with truth table.					L.1	7M		
	c	Subtract using (0-1)'s compliment method $101010100_2 - 1000100_2$ Subtract using 1's compliment method $101010100_2 - 1000100_2$					L.3	6M		
OR										
Q. 06	a	Convert the following: a) $3A6.C58D_{100} = ?$ m b) $0.6875_{100} = ?$ m					L.3	8M		

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LL Leslie

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