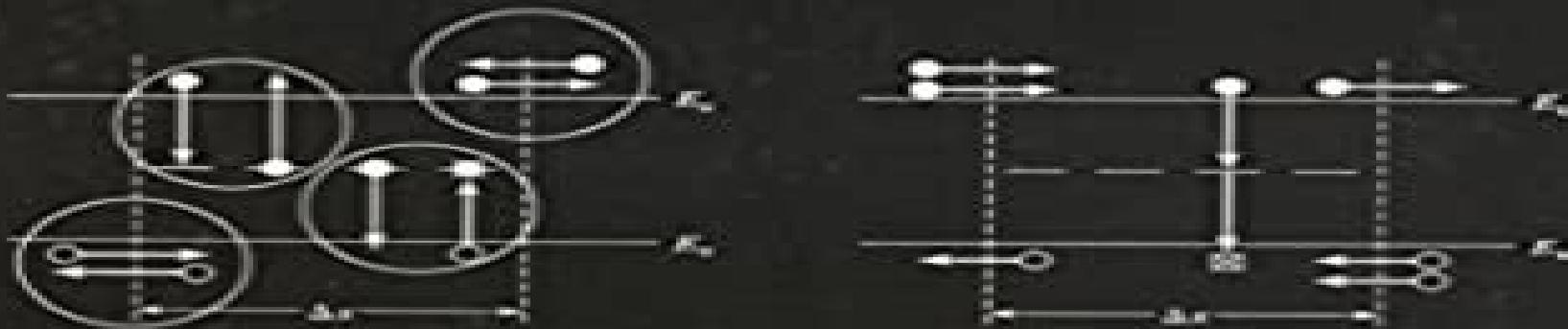


— VOLUME VI —

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SECOND EDITION

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Mosfet Modeling for VLSI Simulation Narain Arora, 2007 A reprint of the classic text this book popularized compact modeling of electronic and semiconductor devices and components for college and graduate school classrooms and manufacturing engineering over a decade ago The first comprehensive book on MOS transistor compact modeling it was the most cited among similar books in the area and remains the most frequently cited today The coverage is device physics based and continues to be relevant to the latest advances in MOS transistor modeling This is also the only book that discusses in detail how to measure device model parameters required for circuit simulations The book deals with the MOS Field Effect Transistor MOSFET models that are derived from basic semiconductor theory Various models are developed ranging from simple to more sophisticated models that take into account new physical effects observed in submicron transistors used in today's 1993 MOS VLSI technology The assumptions used to arrive at the models are emphasized so that the accuracy of the models in describing the device characteristics are clearly understood Due to the importance of designing reliable circuits device reliability models are also covered

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of the MOS Transistor Yannis Tsividis, Colin McAndrew, 2011 The MOS Metal Oxide Semiconductor transistor is the most important building block of modern silicon integrated circuits. This book fills an important gap in the literature by presenting a unified treatment of the operation and modeling of the MOS transistor that is complemented with extensive intuitive discussions. The MOS transistor is the dominant VLSI Very Large Scale Integration device and understanding of this device is mandatory for those people planning a career in device physics and modeling as well as in circuit design. Especially important for university courses there is a logical systematic and progressive description that starts with semiconductor fundamentals and builds up to a comprehensive understanding of the basics of MOS transistors. For practicing professionals there are details of nuances observed in MOS transistor behavior and various approaches to modeling these are presented. Detailed derivations are given for modeling dc currents charges for large signal operation small signal operation at low frequencies and high frequencies and noise.

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FinFET Devices for VLSI Circuits and Systems Samar K. Saha, 2020-07-15 To surmount the continuous scaling challenges of MOSFET devices FinFETs have emerged as the real alternative for use as the next generation device for IC fabrication technology. The objective of this book is to provide the basic theory and operating principles of FinFET devices and technology an overview of FinFET device architecture and manufacturing processes and detailed formulation of FinFET electrostatic and dynamic device

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