

Sunil P. Khatri
Kanupriya Gulati *Editors*

Advanced Techniques in Logic Synthesis, Optimizations and Applications

Advanced Techniques In Logic Synthesis Optimizations And Applications

Roger Woods

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Advanced Techniques in Logic Synthesis, Optimizations and Applications Kanupriya Gulati,2010-11-25 This book covers recent advances in the field of logic synthesis and design including Boolean Matching Logic Decomposition Boolean satisfiability Advanced Synthesis Techniques and Applications of Logic Design All of these topics are valuable to CAD engineers working in Logic Design Logic Optimization and Verification Engineers seeking opportunities for optimizing VLSI integrated circuits will find this book as an invaluable reference since there is no existing book that covers this material in a systematic fashion

Advanced Techniques in Logic Synthesis, Optimizations and Applications Kanupriya

Gulati,2011-03-30 *Logic Functions and Equations* Christian Posthoff,Bernd Steinbach,2018-12-31 The expanded and updated 2nd edition of this classic text offers the reader a comprehensive introduction to the concepts of logic functions and equations and their applications across computer science The approach emphasizes a thorough understanding of the fundamental principles as well as numerical and computer based solution methods Updated throughout some major additions for the 2nd edition include an expanded introductory section on logic equations a new chapter on sets lattices and classes of logic functions a new chapter about SAT problems a new chapter about methods to solve extremely complex problems and an expanded section with new decomposition methods utilizing the Boolean Differential Calculus extended to lattices of logic functions The book provides insight into applications across binary arithmetic coding complexity logic design programming computer architecture and artificial intelligence Based on the extensive teaching experience of the authors Logic Functions and Equations is highly recommended for a one or two semester course in computer science and related programs It provides straightforward high level access to these methods and enables sophisticated applications elegantly bridging the gap between mathematics and the theoretical foundations of computer science

Single-Event Effects, from Space to Accelerator Environments Ygor Quadros de Aguiar,Frédéric Wrobel,Jean-Luc Autran,Rubén García Alía,2024-10-17 This book describes the fundamental concepts underlying radiation induced failure mechanisms in electronic components operating in harsh environments such as in space missions or in particle accelerators In addition to providing an extensive overview of the dynamics and composition of different radiation environments the authors discuss the failure mechanisms known as single event effects SEEs and dedicated failure modeling and prediction methodologies Additionally novel radiation hardening by design RHBD techniques at physical layout and circuit levels are described Readers who are newcomers to this field will learn the fundamental concepts of particle interaction physics and electronics hardening design starting from the composition and dynamics of radiation environments and their effects on electronics to the qualification and hardening of components Experienced readers will enjoy the comprehensive discussion of the state of the art in modeling simulation and analysis of radiation effects developed in the recent years especially the outcome of the recent European project RADSAGA Describes both the fundamental concepts underlying radiation effects in electronics and state of the art hardening

methodologies Addresses failure mechanisms known as single event effects SEEs and dedicated failure modeling and prediction methodologies Reveals novel radiation hardening by design RHBD techniques at physical layout and circuit levels Offers readers the first book in which particle accelerator applications will be extensively included in the radiation effects context This is an open access book **Logic Synthesis and Optimization** Tsutomu Sasao,2012-12-06 Logic Synthesis and Optimization presents up to date research information in a pedagogical form The authors are recognized as the leading experts on the subject The focus of the book is on logic minimization and includes such topics as two level minimization multi level minimization application of binary decision diagrams delay optimization asynchronous circuits spectral method for logic design field programmable gate array FPGA design EXOR logic synthesis and technology mapping Examples and illustrations are included so that each contribution can be read independently Logic Synthesis and Optimization is an indispensable reference for academic researchers as well as professional CAD engineers **VLSI and Chip Design** Dr. M.

Maheswaran,Mandadupu Anusha,Bandam Narendra,Modugu Rambabu,2024-05-23 VLSI and Chip Design exploration of Very Large Scale Integration VLSI technology and the intricacies of modern chip design It fundamental principles advanced methodologies and the latest innovations in circuit design fabrication and testing With a focus on digital and analog systems this integrates theoretical concepts with practical applications catering to both beginners and professionals It emphasizes design optimization power efficiency and scalability making it an essential resource for engineers researchers and students aspiring to excel in semiconductor technology and integrated circuit design **Emerging Technology and Architecture for Big-data Analytics** Anupam Chattopadhyay,Chip Hong Chang,Hao Yu,2017-04-19 This book describes the current state of the art in big data analytics from a technology and hardware architecture perspective The presentation is designed to be accessible to a broad audience with general knowledge of hardware design and some interest in big data analytics Coverage includes emerging technology and devices for data analytics circuit design for data analytics and architecture and algorithms to support data analytics Readers will benefit from the realistic context used by the authors which demonstrates what works what doesn't work and what are the fundamental problems solutions upcoming challenges and opportunities Provides a single source reference to hardware architectures for big data analytics Covers various levels of big data analytics hardware design abstraction and flow from device to circuits and systems Demonstrates how non volatile memory NVM based hardware platforms can be a viable solution to existing challenges in hardware architecture for big data analytics

Application-Driven Architecture Synthesis Francky Catthoor,Lars-Gunnar Svensson,2012-12-06 Application Driven Architecture Synthesis describes the state of the art of architectural synthesis for complex real time processing In order to deal with the stringent timing requirements and the intricacies of complex real time signal and data processing target architecture styles and target application domains have been adopted to make the synthesis approach feasible These approaches are also heavily application driven which is illustrated by many realistic demonstrations used as examples in the

book The focus is on domains where application specific solutions are attractive such as significant parts of audio telecom instrumentation speech robotics medical and automotive processing image and video processing TV multi media radar sonar Application Driven Architecture Synthesis is of interest to both academics and senior design engineers and CAD managers in industry It provides an excellent overview of what capabilities to expect from future practical design tools and includes an extensive bibliography

Programmable Logic Handbook Ashok K. Sharma,1998 This practice oriented guide to programming with Field Programmable Logic Devices is the most complete resource on the subject FPLDs are an essential part of today s high performance electronic systems because they save board space use less power and offer quicker turnaround times than traditional integrated circuits However to maximize FPLDs designers must understand and get around the tradeoffs involved This one stop guide addresses the challenges and opportunities through detailed coverage of FPGAs PLDs PLAs and CPLDs the high level description languages VHDL and Verilog test issues and more

Digest of Technical Papers ,2001

Advanced Logic Design Techniques Zhang Dan,2024-02-23 Advanced Logic Design Techniques authored by Zhang Dan delves into sophisticated approaches and cutting edge tactics required for mastering digital system design This thorough guide navigates readers through the complexity of logic circuits delivering in depth insights into optimization synthesis and verification methods With a rigorous blend of theoretical principles and practical applications this book equips both rookie and seasoned engineers with the tools essential to face modern design issues From algorithmic optimizations to sophisticated synthesis approaches Zhang Dan presents a holistic approach to logic design making this book an invaluable resource for students and experts in the field of digital electronics

Proceedings of the ... International Symposium on Hardware/Software Codesign ,2001

Cornell University Courses of Study Cornell University,2007

The Engineering Index Annual ,1988 Since its creation in 1884 Engineering Index has covered virtually every major engineering innovation from around the world It serves as the historical record of virtually every major engineering innovation of the 20th century Recent content is a vital resource for current awareness new production information technological forecasting and competitive intelligence The world s most comprehensive interdisciplinary engineering database Engineering Index contains over 10 7 million records Each year over 500 000 new abstracts are added from over 5 000 scholarly journals trade magazines and conference proceedings Coverage spans over 175 engineering disciplines from over 80 countries Updated weekly

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Advanced Logic Synthesis André Inácio Reis,Rolf Drechsler,2017-11-15 This book provides a single source reference to the state of the art in logic synthesis Readers will benefit from the authors expert perspectives on new technologies and logic synthesis new data structures big data and logic synthesis and convergent logic synthesis The authors describe techniques that will enable readers to take advantage of recent advances in big data techniques and frameworks in order to have better logic synthesis algorithms

FPGA-based Implementation of Signal Processing

Systems Roger Woods,2008-11-24 The automation of the techniques for component architectural synthesis computational models and the reduction of energy consumption to help improve FPGA performance are given in detail FPGA based Implementation of Signal Processing Systems is an important reference for practising engineers and researchers working on the design and development of DSP systems for radio telecommunication information audio visual and security applications Senior level electrical and computer engineering graduates taking courses in signal processing or digital signal processing shall also find this volume of interest BOOK JACKET **Artificial Intelligence Abstracts** ,1989

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