



Advances in Design and Specification Languages for SoCs

Edited by
Pierre Boulet

The ChDL series
 Springer

Advances In Design And Specification Languages For SoCs

Ibrahim (Abe) M. Elfadel, Lutfi Albasha

Advances In Design And Specification Languages For SoCs:

Advances in Design and Specification Languages for SoCs Pierre Boulet,2006-06-30 The seventh book in the CHDL Series is composed of a selection of the best articles from the Forum on Specification and Design Languages FDL 04 FDL is the European Forum to learn and exchange on new trends on the application of languages and models for the design of electronic and heterogeneous systems The forum was structured around four workshops that are all represented in the book by outstanding articles Analog and Mixed Signal Systems UML based System Specification and Design C C Based System Design and Languages for Formal Specification and Verification The Analog and Mixed Signal Systems contributions bring some answers to the difficult problem of co simulating discrete and continuous models of computation The UML based System Specification and Design chapters bring insight into how to use the Model Driven Engineering to design Systems on Chip The C C Based System Design articles mainly explore system level design with SystemC The Languages for Formal Specification and Verification is represented by an invited contribution on the use of temporal assertions for symbolic model checking and simulation And finally chapter in this book contributed by preeminent members of the automotive design industry presents the recent industry standard AutoSAR Overall Advances in Design and Specification Languages for SoCs is an excellent opportunity to catch up with the latest research developments in the field of languages for electronic and heterogeneous system design

Metamodeling-driven IP Reuse for SoC Integration and Microprocessor Design

Deepak A. Mathaikutty,Sandeep Shukla,2009 This cutting edge resource offers you an in depth understanding of metamodeling approaches for the reuse of intellectual properties IPs in the form of reusable design or verification components The book covers the essential issues associated with fast and effective integration of reusable design components into a system on a chip SoC to achieve faster design turn around time Moreover it addresses key factors related to the use of reusable verification IPs for a write once use many times verification strategy another effective approach that can attain a faster product design cycle

Advances in Design and Specification Languages for Embedded Systems

Sorin Alexander Huss,2007-07-19 Design and specification languages are of utmost interest in the area of embedded systems and the Forum on Specification and Design Languages has been once again the main European event for the embedded systems and chip design community Advances in Design and Specification Languages for Embedded Systems is the latest contribution to the Chip Design Languages series and it consists of selected papers presented at the Forum on Specifications and Design Languages FDL 06 in September 2006 FDL an ECSI conference is the premier European forum to present research results exchange experiences and learn about new trends in the application of specification and design languages as well as of associated design and modelling methods and tools for integrated circuits embedded systems and heterogeneous systems Modelling and specification concepts push the development of new methodologies for design and verification to system level they thus provide the means for a model driven design of complex information processing systems in a variety of application

domains **VLSI-SoC: Technologies for Systems Integration** Jürgen Becker, Marcelo De Oliveira Johann, Ricardo Reis, 2011-08-22 This book contains extended and revised versions of the best papers presented at the 17th IFIP WG 10.5 IEEE International Conference on Very Large Scale Integration VLSI SoC 2009 held in Florianopolis Brazil in October 2009 The 8 papers included in the book together with two keynote talks were carefully reviewed and selected from 27 papers presented at the conference The papers cover a wide variety of excellence in VLSI technology and advanced research addressing the current trend toward increasing chip integration and technology process advancements bringing about stimulating new challenges both at the physical and system design levels as well as in the test of these systems

Behavioral Modeling for Embedded Systems and Technologies: Applications for Design and Implementation Gomes, Luis, Fernandes, Jo?o M., 2009-07-31 This book provides innovative behavior models currently used for developing embedded systems accentuating on graphical and visual notations Provided by publisher **Advances in Computer Systems**

Architecture Thambipillai Srikanthan, Jingling Xue, Chip-Hong Chang, 2005-10-19 On behalf of the Program Committee we are pleased to present the proceedings of the 2005 Asia Pacific Computer Systems Architecture Conference ACSAC 2005 held in the beautiful and dynamic country of Singapore This conference was the tenth in its series one of the leading forums for sharing the emerging research findings in this field In consultation with the ACSAC Steering Committee we selected a member Program Committee This Program Committee represented a broad spectrum of research expertise to ensure a good balance of research areas, institutions and experience while maintaining the high quality of this conference series This year's committee was of the same size as last year but had 19 new faces We received a total of 173 submissions which is 14% more than last year Each paper was assigned to at least three and in some cases four Program Committee members for review Wherever necessary the committee members called upon the expertise of their colleagues to ensure the highest possible quality in the reviewing process As a result we received 415 reviews from the Program Committee members and their 105 co-reviewers whose names are acknowledged in the proceedings The conference committee adopted a systematic blind review process to provide a fair assessment of all submissions In the end we accepted 65 papers on a broad range of topics giving an acceptance rate of 37.5% We are grateful to all the Program Committee members and the co-reviewers for their efforts in completing the reviews within a tight schedule **UML for SOC Design** Grant Martin, Wolfgang Müller, 2006-07-01 A tutorial approach to using the UML modeling language in system-on-chip design Based on the DAC 2004 tutorial applicable for students and professionals Contributions by top level international researchers The best work at the first UML for SoC workshop Unique combination of both UML capabilities and SoC design issues Condenses research and development ideas that are only found in multiple conference proceedings and many other books into one place Will be the seminal reference work for this area for years to come

Proceedings of the IASTED International Conference on Advances in Computer Science and Technology, November 22-24, 2004, St. Thomas, US Virgin Islands, 2004 **VLSI-SoC 2023: Innovations for**

Trustworthy Artificial Intelligence Ibrahim (Abe) M. Elfadel, Lutfi Albasha, 2024-12-28 This book contains revised and extended versions of a selection of papers presented at the 31st IFIP WG 10.5 IEEE International Conference on the theme of VLSI SoC Innovations for Trustworthy Artificial Intelligence VLSI SoC 2023 held in Sharjah United Arab Emirates during October 16-18 2023. The 15 full papers included in this volume were carefully revised and expanded from 77 papers submitted to the conference. This edited volume has been organized into four parts: architectures, accelerators, resiliency, and robustness and security and privacy, and is representing cutting-edge research at the forefront of VLSI technology.

Advances in Design Methods from Modeling Languages for Embedded Systems and SoC's Dominique Borrione, 2010. More than ever, FDL is the place for researchers, developers, industry, designers, academia, and EDA tool companies to present and to learn about the latest scientific achievements, practical applications, and users' experiences in the domain of specification and design languages. FDL covers the modeling and design methods and their latest supporting tools for complex embedded systems, systems-on-chip, and heterogeneous systems. FDL 2009 is the twelfth in a series of events that were held all over Europe in selected locations renowned for their Universities and Research Institutions, as well as the importance of their industrial environment in Computer Science and Microelectronics. In 2009, FDL was organized in the attractive south of France area of Sophia Antipolis, together with the DASIP Design and Architectures for Signal and Image Processing Conference and the SAME Sophia Antipolis MicroElectronics Forum. All submitted papers were carefully reviewed to build a program with 27 full and 10 short contributions. From these, the Program Committee selected a shorter list based on the evaluations of the reviewers and the originality and relevance of the work that was presented at the Forum. The revised and sometimes extended versions of these contributions constitute the chapters of this volume. *Advances in Design Methods from Modeling Languages for Embedded Systems and SoC's* presents extensions to standard specification and description languages as well as new language-based design techniques and methodologies to solve the challenges raised by mixed signal and multi-processor systems on a chip. It is intended as a reference for researchers and lecturers, as well as a state-of-the-art milestone for designers and CAD developers.

Proceedings American Society of Civil Engineers, 1913

Proceedings of the American Society of Civil Engineers American Society of Civil Engineers, 1929. Vols. for Jan 1896-Sept 1930 contain a separately page section of Papers and discussions which are published later in revised form in the society's Transactions. Beginning Oct 1930, the Proceedings are limited to technical papers and discussions, while Civil engineering contains items relating to society activities, etc.

ASICON 2003 Ting-Ao Tang, 2003

EUROMICRO 99, Proceedings of the 25th EUROMICRO Conference, 1999. Annotation: This is a two-volume set of the proceedings of the September 1999 conference on the current and future developments in informatics theories and application areas. Volume I: 80 contributions discuss digital system design architectures and methods and tools. Volume II: 30 contributions cover music technology and audio processing, dependable computing systems, software process and product improvement.

multimedia and telecommunication and network computing Lacks a subject index Annotation copyrighted by Book News Inc
Portland OR Digest of Technical Papers ,2001 Proceedings, ... International Symposium on VLSI Design ,2003

Advances in Design Methods from Modeling Languages for Embedded Systems and SoC's Dominique

Borrione,2010-08-24 More than ever FDL is the place for researchers developers industry designers academia and EDA tool companies to present and to learn about the latest scientific achievements practical applications and users experiences in the domain of specification and design languages FDL covers the modeling and design methods and their latest supporting tools for complex embedded systems systems on chip and heterogeneous systems FDL 2009 is the twelfth in a series of events that were held all over Europe in selected locations renowned for their Universities and Research Institutions as well as the importance of their industrial environment in Computer Science and Micro electronics In 2009 FDL was organized in the attractive south of France area of Sophia Antipolis together with the DASIP Design and Architectures for Signal and Image Processing Conference and the SAME Sophia Antipolis MicroElectronics Forum All submitted papers were carefully reviewed to build a program with 27 full and 10 short contributions From these the Program Committee selected a shorter list based on the evaluations of the reviewers and the originality and relevance of the work that was presented at the Forum The revised and sometimes extended versions of these contributions constitute the chapters of this volume **Advances in Design Methods from Modeling Languages for Embedded Systems and SoC's** presents extensions to standard specification and description languages as well as new language based design techniques and methodologies to solve the challenges raised by mixed signal and multi processor systems on a chip It is intended as a reference for researchers and lecturers as well as a state of the art milestone for designers and CAD developers **Applications of Specification and Design Languages for SoCs** A. Vachoux,2006-10-06 This book includes a selection of the best contributions to the Forum on Specification and Design Languages held in 2005 FDL 05 It provides detailed insights into recent works dealing with a large spectrum of issues in system on chip design All the chapters have been carefully revised and extended to offer up to date information They also provide seeds for further researches and developments in the field of heterogeneous systems on chip design

International Symposium on System Synthesis ,2001 **SASIMI '98** ,1998

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