

EXPERIEMENT NO. 3

Simulation using all the modeling styles and Synthesis of 2:1 Multiplexer and 4:1 Multiplexer using VERILOG HDL

Aim:

Perform Zero Delay Simulation of 2:1 Multiplexer and 4:1 Multiplexer written in behavioral, dataflow and structural modeling style in VERILOG HDL using a Test bench. Then, Synthesize each one of them on two different EDA tools.

Electronics Design Automation Tools used:

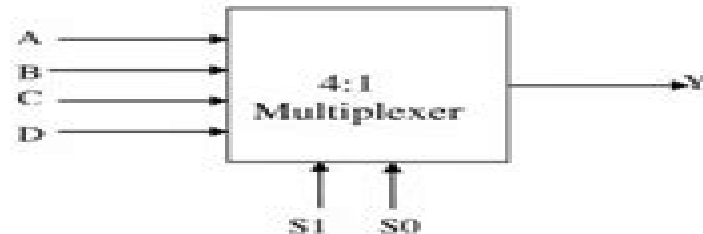
- i) FPGA Advantage 3.1 (includes Model Sim simulation tool and Leonardo Spectrum Synthesis Tool)
- ii) Xilinx Project Navigator 8.1 (Includes all the steps in the design flow from Simulation to Implementation to download onto FPGA).

Block Diagram:

2:1 Multiplexer:



4:1 Multiplexer:



Truth table:

2:1 Multiplexer:

S	A	B	Y
0	0	0	0

Arun Kumar Vhdl Lab Manual

David R. Coelho



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Digital Electronics Lab Manual with Vhdl Scott Stewart, 2004-11-30 *Programming Digital Applications W/Able, Vhdl-Lab Manual* Han-Way Huang, 2006-07 **Digital Fundamentals with Cplds, Fpgas and Vhdl Laboratory Manual** David Buchla, Douglas Joks, 2002-09-30 *A Guide to VHDL* Stanley Mazor, Patricia Langstraat, 2013-06-29

A Guide to VHDL is intended for the working engineer who needs to develop document simulate and synthesize a design using the VHDL language It is for system and chip designers who are working with VHDL CAD tools and who have some experience programming in Fortran Pascal or C and have used a logic simulator A Guide to VHDL includes a number of paper exercises and computer lab experiments If a compiler simulator is available to the reader then the lab exercises included in the chapters can be run to reinforce the learning experience For practical purposes this book keeps simulator specific text to a minimum but does use the Synopsys VHDL Simulator command language in a few cases A Guide to VHDL can be used as a primer since its contents are appropriate for an introductory course in VHDL

The Student's Guide to VHDL Peter J. Ashenden, 2008-07-01 The Student's Guide to VHDL is a condensed edition of The Designer's Guide to VHDL the most widely used textbook on VHDL for digital system modeling The Student's Guide is targeted as a supplemental reference book for computer organization and digital design courses Since publication of the first edition of The Student's Guide the IEEE VHDL and related standards have been revised The Designer's Guide has been revised to reflect the changes so it is appropriate that The Student's Guide also be revised In The Student's Guide to VHDL 2nd Edition we have included a design case study illustrating an FPGA based design flow The aim is to show how VHDL modeling fits into a design flow starting from high level design and proceeding through detailed design and verification synthesis FPGA place and route and final timing verification Inclusion of the case study helps to better serve the educational market Currently most college courses do not formally address the details of design flow Students may be given informal guidance on how to proceed with lab projects In many cases it is left to students to work it out for themselves The case study in The Student's Guide provides a reference design flow that can be adapted to a variety of lab projects

The Designer's Guide to VHDL Peter J. Ashenden, 2002 Since the publication of the first edition of The Designer's Guide to VHDL in 1996 digital electronic systems have increased exponentially in their complexity product lifetimes have dramatically shrunk and reliability requirements have shot through the roof As a result more and more designers have turned to VHDL to help them dramatically improve productivity as well as the quality of their designs VHDL the IEEE standard hardware description language for describing digital electronic systems allows engineers to describe the structure and specify the function of a digital system as well as simulate and test it before manufacturing In addition designers use VHDL to synthesize a more detailed structure of the design freeing them to concentrate on more strategic design decisions and reduce time to market Adopted by designers around the world the VHDL family of standards have recently been revised to address a range of issues including portability across synthesis tools This

best selling comprehensive tutorial for the language and authoritative reference on its use in hardware design at all levels from system to gates has been revised to reflect the new IEEE standard VHDL 2001 Peter Ashenden a member of the IEEE VHDL standards committee presents the entire description language and builds a modeling methodology based on successful software engineering techniques Reviewers on Amazon com have consistently rated the first edition with five stars This second edition updates the first retaining the authors unique ability to teach this complex subject to a broad audience of students and practicing professionals **Introduction to VHDL** ,1997 **The VHDL Handbook** David R.

Coelho,1989-06-30 This book is intended to be a working reference for electronic hardware designers who are interested in writing VHDL models A handbook cookbook approach is taken with many complete examples used to illustrate the features of the VHDL language and to provide insight into how particular classes of hardware devices can be modelled in VHDL It is possible to use these models directly or to adapt them to similar problems with minimal effort This book is not intended to be a complete reference manual for the VHDL language It is possible to begin writing VHDL models with little background in VHDL by copying examples from the book and adapting them to particular problems Some exposure to the VHDL language prior to using this book is recommended The reader is assumed to have a solid hardware design background preferably with some simulation experience For the reader who is interested in getting a complete overview of the VHDL language the following publications are recommended reading An Introduction to VHDL Hardware Description and Design LIP89 IEEE Standard VHDL Language Reference Manual IEEE87 Chip Level Behavioral Modelling ARMS88 Multi Level Simulation of VLSI Systems COEL87 Other references of interest are USG88 DOD88 and CLSI87 Use of the Book If the reader is familiar with VHDL the models described in chapters 3 through 7 can be applied directly to design problems Comprehensive VHDL ,2006 VHDL Starter's Guide Sudhakar Yalamanchili,1998 VHDL Starter's Guide has been written for the student and practitioner alike as a clear and concise tutorial on VHDL VHSIC Hardware Description Language It provides a hands on step by step introduction to learning VHDL as an applied language to be used in the design and testing of digital logic networks Command syntax and structure are emphasized and the writing is based on many examples of real world logic circuits **VHDL: Programming by Example** Douglas L. Perry,2002-06-02 Teaches VHDL by example Includes tools for simulation and synthesis CD ROM containing Code Design examples and a working demo of ModelSIM **Instructor's Resource Manual to Accompany Digital Fundamentals with VHDL** Thomas L. Floyd,2003 **Instructor's Solutions Manual to Accompany Fundamentals of Digital Logic with Vhdl Design** Lord Brown,Stephen D. Brown,Zvonko G. Vranesic,1999-10-01 **Digital Logic Discrete Lab Manual** Gary E. Jones,1979* **Digital Logic I.C. Lab Manual** Gary E. Jones,1978*

Reviewing **Arun Kumar Vhdl Lab Manual**: Unlocking the Spellbinding Force of Linguistics

In a fast-paced world fueled by information and interconnectivity, the spellbinding force of linguistics has acquired newfound prominence. Its capacity to evoke emotions, stimulate contemplation, and stimulate metamorphosis is really astonishing. Within the pages of "**Arun Kumar Vhdl Lab Manual**," an enthralling opus penned by a very acclaimed wordsmith, readers embark on an immersive expedition to unravel the intricate significance of language and its indelible imprint on our lives. Throughout this assessment, we shall delve in to the book is central motifs, appraise its distinctive narrative style, and gauge its overarching influence on the minds of its readers.

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