最新のインターフェース

ユーザビリティを考慮した最新のUIで設計作業を効率化

ツールとデザイナー

たったひとつの簡単なタスクを実行するだけなのに、ソフトウェアと何時間も格闘してしまったという経験はないですか?

これは、あなただけに限ったことではありません。多くのユーザーが、古くて複雑で、紛らわしいユーザーインターフェースに対応しなければならないことにストレスを感じています。自分の頭をデスクにぶつけてみたり、サポートの処理やオンラインでの調査にさらに何時間も費やしたりしてようやく面倒な回避策を見つけて設計に戻るわけですが、それほどの手間をかける価値が本当にあるのでしょうか?

トレーニングが業務に及ぼす効果

使いやすく設計されていないユーザーインターフェースは、ユーザー、仕事、仕事と生活のバランスに大きな影響を与える可能性があります。たったひとつの設計変更のために何時間もの作業をやり直さなければならなかったことが今までに何度ありましたか?あるいは、簡単でも動作するには必要な機能を得るために半日を無駄にしたことは?問題が何であれ、結果は同じです。他のタスクを遅らせて、複雑な回避策を作成し、重要な設計マイルストーンすら見失っています。

電子製品の設計は、難しいが興味深く、やりがいのあるものであり、もちろん楽しいものであるべきです。ですが、あなたのツールが思うように実行するどころかまったく思うように動作しない場合、そのプロセスはひどく辛いものとなります。問題のあるツールは悲惨な結果をもたらすことがあります。

- 締め切りに間に合わない
- 基板の再設計
- 投資家/顧客を失う

これらの問題は大げさではありません。ひとつの基板を再設計することによって、小規模または中規模の事業であっても破産につながる可能性があります。PCB設計者は、会社を生み出すこともあるために失うこともあり得ます。各マイルストーンの最中に大きな投資が行われているときに締め切りを逃すことによってもプロジェクトを損なう可能性があります。

もしも…

設計の締め切りに間に合わなかったり、フラストレーションからコンピューターを殴りたいと思ったことはありませんか。

その結果。

最終的には、真のボトルネックであり、真の革新に対する障壁であるツールチェーン自体に対応する必要があります。

もしも…

仕事に行く際に設計ツールと格闘し、電子機器の設計への情熱を再燃させる必要がないとしたら、どうでしょうか?Altium Designerを使用すると、それが可能です。

- 探しているものを見つける
- 動作する機能のみを備える
- 生産的になる
- フラストレーションを回避
- 期限を守る

できること

PCBの設計とアンインストールに使用する3つの異なるプログラムを閉じます。Altium Designerには、設計プロセスのあらゆる面で一貫していて、インテリジェントに設計された、簡単で最新の、強力なユーザーインターフェイスがあります。これは、メニューと機能の論理的な配置が設計プロセス全体で表示されるため、ツールをさらに迅速に習得できるということです。

[Altium Designer User Manual](#)

Eric Bogatin

Altium Designer User Manual:

FPGAs for Software Programmers Dirk Koch,Frank Hannig,Daniel Ziener,2016-06-17 This book makes powerful Field Programmable Gate Array FPGA and reconfigurable technology accessible to software engineers by covering different state of the art high level synthesis approaches e g OpenCL and several C to gates compilers It introduces FPGA technology its programming model and how various applications can be implemented on FPGAs without going through low level hardware design phases Readers will get a realistic sense for problems that are suited for FPGAs and how to implement them from a software designer s point of view The authors demonstrate that FPGAs and their programming model reflect the needs of stream processing problems much better than traditional CPU or GPU architectures making them well suited for a wide variety of systems from embedded systems performing sensor processing to large setups for Big Data number crunching This book serves as an invaluable tool for software designers and FPGA design engineers who are interested in high design productivity through behavioural synthesis domain specific compilation and FPGA overlays Introduces FPGA technology to software developers by giving an overview of FPGA programming models and design tools as well as various application examples Provides a holistic analysis of the topic and enables developers to tackle the architectural needs for Big Data processing with FPGAs Explains the reasons for the energy efficiency and performance benefits of FPGA processing Provides a user oriented approach and a sense for where and how to apply FPGA technology *400+ Electronic Design EngineerInterview Questions And Answers Guide (expert Edition) – Complete Study & Preparation Ebook* CloudRoar Consulting services,101-01-01 Prepare for the Zscaler Certified Administrator exam with 350 questions and answers covering cloud security firewall policies access control traffic inspection logging and best practices Each question provides practical examples and detailed explanations to ensure exam readiness Ideal for cloud security engineers and administrators Zscaler CertifiedAdministrator CloudSecurity FirewallPolicies AccessControl TrafficInspection Logging BestPractices ExamPreparation CareerGrowth ProfessionalDevelopment ITSecurity CloudEngineering ITSkills ITCertifications **A Hands-On Guide to Designing Embedded Systems** Adam Taylor,Dan Binnun,Saket Srivastava,2021-10-31 This practical resource introduces readers to the design of field programmable gate array systems FPGAs Techniques and principles that can be applied by the engineer to understand challenges before starting a project are presented The book provides a framework from which to work and approach development of embedded systems that will give readers a better understanding of the issues at hand and can develop solution which presents lower technical and programmatic risk and a faster time to market Programmatic and system considerations are introduced providing an overview of the engineering life cycle when developing an electronic solution from concept to completion Hardware design architecture is discussed to help develop an architecture to meet the requirements placed upon it and the trade offs required to achieve the budget The FPGA development lifecycle and the inputs and outputs from each stage including design test benches synthesis mapping place and

route and power estimation are also presented Finally the importance of reliability why it needs to be considered the current standards that exist and the impact of not considering this is explained Written by experts in the field this is the first book by engineers in the trenches that presents FPGA design on a practical level   Bogatin's Practical Guide to Prototype Breadboard and PCB Design Eric Bogatin,2021-09-30 Printed circuit boards PCB are at the heart of every electronic product manufactured today Yet engineers rarely learn to design PCBs from a class or course They learn it by doing by reading app notes watching YouTube videos and sitting by the side of an experienced engineer This book is the foundation building book for all engineers starting out to design PCBs It teaches good habits designing a PCB first for connectivity and secondly introduces the four most important principles to reduce noise A seven step process is presented developing a plan of record creating a Bill of Materials completing the schematic completing the layout completing the assembly conducting bring up and troubleshooting and documenting the project Each step is developed in detail In particular the emphasis in this book is on risk management what can be done at each step of the process to reduce the risk of a hard error which requires a complete re spin or a soft error which requires some sort of on the fly repair After connectivity is designed it s important to develop good habits to minimize the potential noise from ground bounce power rail stitching noise stack up design and reducing switching noise in signal paths These techniques apply to all designs from 2 layer to 8 layer and more for bandwidths below 200 MHz The best practices for manual lead free soldering are presented so that everyone can become a soldering expert The best measurement practices using common lab instruments such as the DMM the constant current constant voltage power supply and oscilloscopes are presented so that common artifacts are minimized Features in the design that help you find design or assembly errors quickly and the troubleshooting techniques to find and fix problems are introduced Applying the habits presented in this book will help every engineer design their next circuit board faster with less chance of an unexpected problem with the lowest noise This textbook will also have embedded videos to visually demonstrate many of the hands on processes introduced in this book   *Printed Circuits Handbook, Seventh Edition* Clyde F. Coombs,Happy Holden,2016-02-15 The world s leading guide to printed circuits completely updated to include the latest tools technology and techniques The de facto industry standard for over 30 years this practical guide equips you with definitive coverage of every facet of printed circuit assemblies from design methods to fabrication processes Now thoroughly revised and updated this book offers cutting edge coverage of printed circuit engineering fabrication construction soldering testing and repair Printed Circuits Handbook Seventh Edition features all new critical guidance on how to create manage and measure performance throughout the global supply chain Written by a team of international experts from both industry and academia this comprehensive volume offers new information on geographical specialization as well as the latest phase of the EUs Directive on the Restriction of Hazardous Substances ROHS II Fully overhauled to cover the latest scientific and technical developments Brand new coverage of printed circuit supply chain technology and geographical specialization Complete

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