Kingshuk Karuri · Rainer Leupers

Application Analysis Tools for ASIP Design

Application Profiling and Instruction-set Customization

Application Analysis Tools For Asip Design Application Profiling And Instruction Set Customization

Ying Liu

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    Application Analysis Tools for ASIP Design Kingshuk Karuri,Rainer Leupers,2011-06-15 This book introduces a novel design methodology which can significantly reduce the ASIP development effort through high degrees of design automation The key elements of this new design methodology are a powerful application profiler and an automated instruction set customization tool which considerably lighten the burden of mapping a target application to an ASIP architecture in the initial design stages The book includes several design case studies with real life embedded applications to demonstrate how the methodology and the tools can be used in practice for accelerating the overall ASIP design process **Application Analysis Tools for Asip Design** Kingshuk Karuri,Rainer Leupers,2011-06-01 Computational Science and Computational Intelligence Hamid R. Arabnia,Leonidas Deligiannidis,Farzan Shenavarmasouleh,Soheyla Amirian,Farid Ghareh Mohammadi,2025-09-12 This CCIS book constitutes selected papers accepted in the Research Track on Computational Science and the Research Track on Computational Intelligence held as part of the 11th International Conference on Computational Science and Computational Intelligence CSCI 2024 which took place in Las Vegas NV USA during December 11 13 2024 The 24 full papers included in this book were carefully reviewed and selected from a total of 78 submissions The Research Track on Computational Science CSCI RTSC received 78 submissions of which 17 papers were accepted 16 of these are included in this volume For the Research Track on Computational Intelligence CSCI RTCI 10 papers were accepted from 59 submissions The contributions were organized in topical sections on Computational Science Frameworks Applications and Algorithms Computational Science Mathematics Simulation Performance Studies Optimization and Programming and Computational Intelligence Optimization Applications and Algorithms **Customizable Embedded Processors** Paolo Ienne,Rainer Leupers,2006-08-30 Customizable processors have been described as the next natural step in the evolution of the microprocessor business a step in the life of a new technology where top performance alone is no longer sufficient to guarantee market success Other factors become fundamental such as time to market convenience energy efficiency and ease of customization This book is the first to explore comprehensively one of the most fundamental trends which emerged in the last decade to treat processors not as rigid fixed entities which designers include as is in their products but rather to build sound methodologies to tailor fit processors to the specific needs of such products This book addresses the goal of maintaining a very large family of processors with a wide range of features at a cost comparable to that of maintaining a single processor First book to present comprehensively the major ASIP design methodologies and tools without any particular bias Written by most of the pioneers and top international experts of this young domain Unique mix of management perspective technical detail research outlook and practical implementation *Energy-Efficient Communication Processors* Robert Fasthuber,Francky Catthoor,Praveen Raghavan,Frederik Naessens,2013-05-29 This book describes a new design approach for energy efficient Domain Specific Instruction set Processor DSIP architectures for the wireless baseband

domain The innovative techniques presented enable co design of algorithms architectures and technology for efficient implementation of the most advanced technologies To demonstrate the feasibility of the author s design approach case studies are included for crucial functionality of advanced wireless systems with increased computational performance flexibility and reusability Designers using this approach will benefit from reduced development product costs and greater scalability to future process technology nodes **Pipelined Multiprocessor System-on-Chip for Multimedia** Haris Javaid,Sri Parameswaran,2013-11-26 This book describes analytical models and estimation methods to enhance performance estimation of pipelined multiprocessor systems on chip MPSoCs A framework is introduced for both design time and run time optimizations For design space exploration several algorithms are presented to minimize the area footprint of a pipelined MPSoC under a latency or a throughput constraint A novel adaptive pipelined MPSoC architecture is described where idle processors are transitioned into low power states at run time to reduce energy consumption Multi mode pipelined MPSoCs are introduced where multiple pipelined MPSoCs optimized separately are merged into a single pipelined MPSoC enabling further reduction of the area footprint by sharing the processors and communication buffers Readers will benefit from the authors combined use of analytical models estimation methods and exploration algorithms and will be enabled to explore billions of design points in a few minutes **Multi-Core Embedded Systems** Georgios Kornaros,2018-10-08 Details a real world product that applies a cutting edge multi core architecture Increasingly demanding modern applications such as those used in telecommunications networking and real time processing of audio video and multimedia streams require multiple processors to achieve computational performance at the rate of a few giga operations per second This necessity for speed and manageable power consumption makes it likely that the next generation of embedded processing systems will include hundreds of cores while being increasingly programmable blending processors and configurable hardware in a power efficient manner Multi Core Embedded Systems presents a variety of perspectives that elucidate the technical challenges associated with such increased integration of homogeneous processors and heterogeneous multiple cores It offers an analysis that industry engineers and professionals will need to understand the physical details of both software and hardware in embedded architectures as well as their limitations and potential for future growth Discusses the available programming models spread across different abstraction levels The book begins with an overview of the evolution of multiprocessor architectures for embedded applications and discusses techniques for autonomous power management of system level parameters It addresses the use of existing open source and free tools originating from several application domains such as traffic modeling graph theory parallel computing and network simulation In addition the authors cover other important topics associated with multi core embedded systems such as Architectures and interconnects Embedded design methodologies Mapping of applications Journal of VLSI Signal Processing Systems for Signal, Image, and Video Technology ,2007 **Energy Efficient Computing & Electronics** Santosh K. Kurinec,Sumeet Walia,2019-01-31 In our abundant computing

infrastructure performance improvements across most all application spaces are now severely limited by the energy dissipation involved in processing storing and moving data The exponential increase in the volume of data to be handled by our computational infrastructure is driven in large part by unstructured data from countless sources This book explores revolutionary device concepts associated circuits and architectures that will greatly extend the practical engineering limits of energy efficient computation from device to circuit to system level With chapters written by international experts in their corresponding field the text investigates new approaches to lower energy requirements in computing Features Has a comprehensive coverage of various technologies Written by international experts in their corresponding field Covers revolutionary concepts at the device circuit and system levels **A Practical Approach to Real-time Systems** Phillip A. Laplante,2000 Under the same cover this volume offers both modern and classic papers focusing on real time systems design and analysis Rather than focusing in theoretical observations of real time systems it is intended for the practical professional who is building real real time systems The editor himself the author of a course on real time systems has selected articles to provide a deep exploration of issues raised in his other works In particular emphasis is placed on applying practical but theoretically sound approaches in software engineering rate monotonic design and analysis testing and architecting systems for real time applications Real-time Signal and Image Processing Phillip A. Laplante,2000 An introduction to intermediate readings in real time image and signal processing It covers issues and challenges hardware support algorithms software languages and systems and applications and case studies **Proceedings of the ... International Symposium on Hardware/Software Codesign** ,2002 **CODES 2002** ,2002 IEICE Transactions on Electronics ,2008 **XIII SBMicro, International Conference on Microelectronics and Packaging, ICMP'98: Advanced courses and invited papers** Ivan Jorge Chueiri,Carlos Alberto dos Reis Filho,1998 **Computer & Control Abstracts** ,1996 Shade Bob Cmelik,David Keppel,1993 Abstract Shade is an instruction set simulator and custom trace generator Application programs are executed and traced under the control of a user supplied trace analyzer To reduce communication costs Shade and the analyzer are run in the same address space To further improve performance code which simulates and traces the application is dynamically generated and cached for reuse Current implementations run on SPARC systems and to varying degrees simulate the SPARC Version 8 and 9 and MIPS I instruction sets This paper describes the capabilities design implementation and performance of Shade and discusses instruction set emulation in general

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