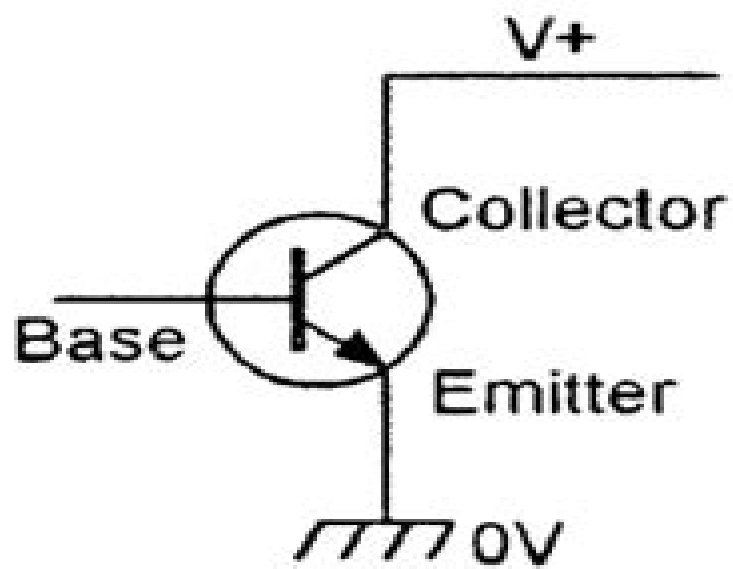
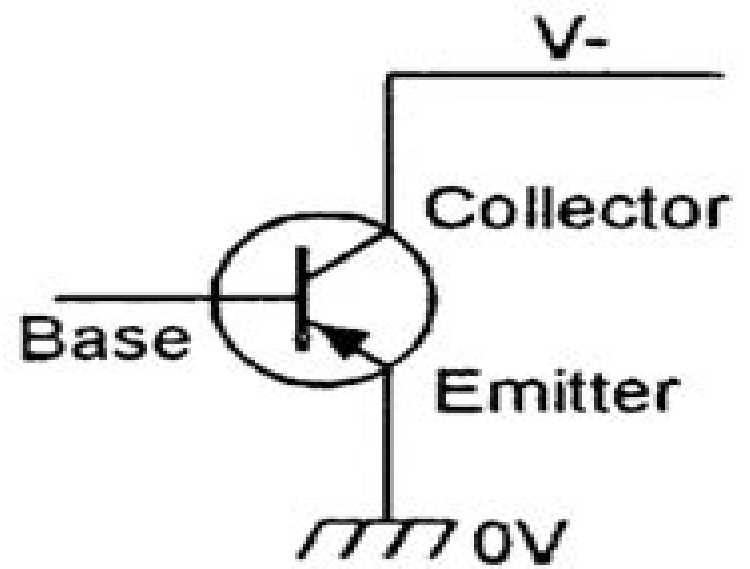
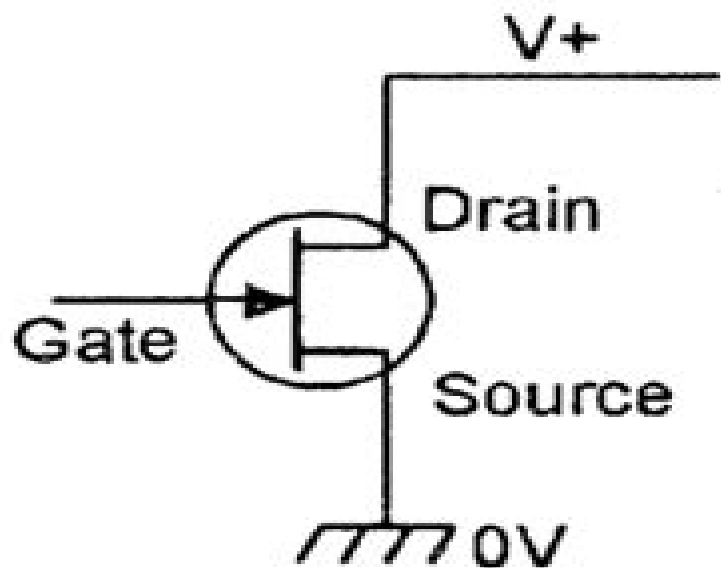




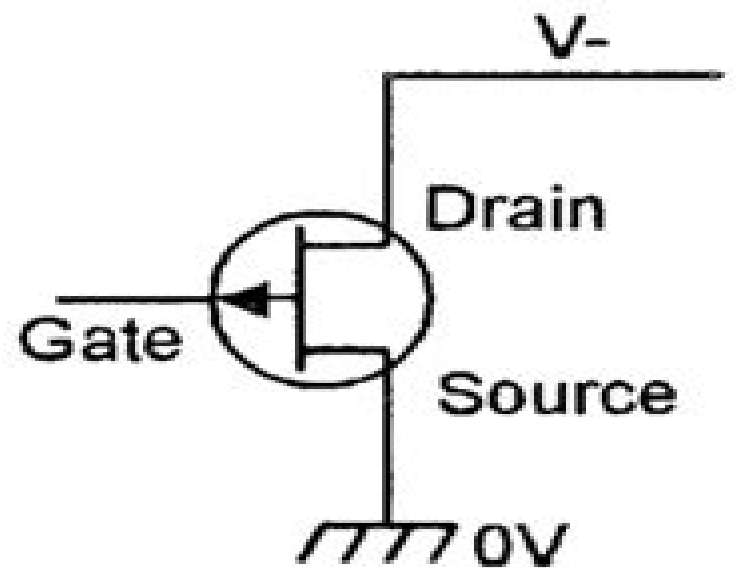
(a) npn transistor



(b) pnp transistor



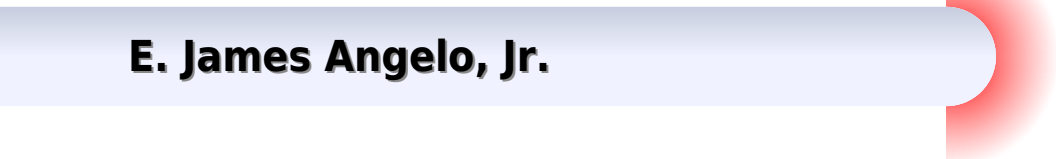
(c) n-channel JFET



(d) p-channel JFET

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Acknowledgements I wish to express my gratitude to Valid Logic Systems Inc

Device Circuit Co-Design Issues in FETs Shubham Tayal, Billel Smaani, Shiromani Balmukund Rahi, Samir Labiod, Zeinab Ramezani, 2023-08-22 This book provides an overview of emerging semiconductor devices and their applications in electronic circuits which form the foundation of electronic devices Device Circuit Co Design Issues in FETs provides readers with a better understanding of the ever growing field of low power electronic devices and their applications in the wireless biosensing and circuit domains The

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