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Algorithm Design Jon Kleinberg, Éva Tardos,2006
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Artificial Neural Nets and Genetic Algorithms George D. Smith,Nigel C. Steele,1998 This is the third in a series of conferences devoted primarily to the theory and applications of artificial neural networks and genetic algorithms The first such event was held in Innsbruck Austria in April 1993 the second in Ales France in April 1995 We are pleased to host the 1997 event in the mediaeval city of Norwich England and to carryon the fine tradition set by its predecessors of providing a relaxed and stimulating environment for both established and emerging researchers working in these and other related fields This series of conferences is unique in recognising the relation between the two main themes of artificial neural networks and genetic algorithms each having its origin in a natural process fundamental to life on earth and each now well established as a paradigm fundamental to continuing technological development through the solution of complex industrial commercial and financial problems This is well illustrated in this volume by the numerous applications of both paradigms to new and challenging problems The third key theme of the series therefore is the integration of both technologies either through the use of the genetic algorithm to construct the most effective network architecture for the problem in hand or more recently the use of neural networks as approximate fitness functions for a genetic algorithm searching for good solutions in an incomplete solution space i e one for which the fitness is not easily established for every possible solution instance

Hybrid Optimization Techniques Narendra Jussien,2004 *International Symposium on Quality Electronic Design* ,2002 Annotation Fifty one papers and 21 posters from the March 2002 symposium report current research in deep submicron integrated circuit design and development The sessions address interconnect extraction and modeling design for process variations metrics power and noise management verification signal integrity and low power design techniques Some of the topics are transition aware global signaling TAGS the interoperability of EDA tools for sequential logic synthesis statistical methods for the determination of process corners power supply noise suppression via clock skew scheduling and the relation between SAT and BDDs for equivalence checking No subject index Annotation copyrighted by Book News Inc Portland OR Government Reports Announcements & Index ,1988 *Proceedings of ASP-DAC/VLSI Design 2002* ,2002 Papers from a January 2002 conference are organized into four sessions each on low

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