

*ACM Doctoral Dissertation
Award 1985*

**Bulldog:
A Compiler for VLIW
Architectures**

John R. Ellis

The MIT Press

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Association for Computing Machinery

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Multithreaded Computer Architecture: A Summary of the State of the ART Robert A. Iannucci,Guang R. Gao,Robert H. Halstead Jr.,Burton Smith,2012-12-06 Multithreaded computer architecture has emerged as one of the most promising and exciting avenues for the exploitation of parallelism This new field represents the confluence of several independent research directions which have united over a common set of issues and techniques Multithreading draws on recent advances in dataflow RISC compiling for fine grained parallel execution and dynamic resource management It offers the hope of dramatic performance increases through parallel execution for a broad spectrum of significant applications based on extensions to traditional approaches Multithreaded Computer Architecture is divided into four parts reflecting four major perspectives on the topic Part I provides the reader with basic background information definitions and surveys of work which have in one way or another been pivotal in defining and shaping multithreading as an architectural discipline Part II examines key elements of multithreading highlighting the fundamental nature of latency and synchronization This section presents clever techniques for hiding latency and supporting large synchronization name spaces Part III looks at three major multithreaded systems considering issues of machine organization and compilation strategy Part IV concludes the volume with an analysis of multithreaded architectures showcasing methodologies and actual measurements Multithreaded Computer Architecture A Summary of the State of the Art is an excellent reference source and may be used as a text for advanced courses on the subject *Hypercube Multiprocessors, 1987* Michael T. Heath,1987-01-01 Proceedings Parallel Computing

High-Level Synthesis for Real-Time Digital Signal Processing Jan Vanhoof,Karl Van Rompaey,Ivo Bolsens,Gert Goossens,Hugo De Man,2012-12-06 High Level Synthesis for Real Time Digital Signal Processing is a comprehensive reference work for researchers and practicing ASIC design engineers It focuses on methods for compiling complex low to medium throughput DSP system and on the implementation of these methods in the CATHEDRAL II compiler The emergence of independent silicon foundries the reduced price of silicon real estate and the shortened processing turn around time bring silicon technology within reach of system houses Even for low volumes digital systems on application specific integrated circuits ASICs are becoming an economically meaningful alternative for traditional boards with analogue and digital commodity chips ASICs cover the application region where inefficiencies inherent to general purpose components cannot be tolerated However full custom handcrafted ASIC design is often not affordable in this competitive market Long design times a high development cost for a low production volume the lack of silicon designers and the lack of suited design facilities are inherent difficulties to manual full custom chip design To overcome these drawbacks complex systems have to be integrated in ASICs much faster and without losing too much efficiency in silicon area and operation speed compared to handcrafted chips The gap between system design and silicon design can only be bridged by new design CAD The idea of a silicon compiler translating a behavioural system specification directly into silicon was born from the awareness that the

ability to fabricate chips is indeed outrunning the ability to design them At this moment CAD is one order of magnitude behind schedule Conceptual CAD is the keyword to mastering the design complexity in ASIC design and the topic of this book

Bulldog John Rolfe Ellis,1986 This thesis explores the problem of exploiting the parallelism available in emerging multiprocessor architectures Ellis contends that high quality compilers can be written for VLIW Very Long Instruction Word computers These machines drive multiple parallel RISCs Reduced Instruction Set Computers with a single instruction stream Each instruction is long enough to command all of the RISCs at once Exploiting the parallelism in VLIW machines can be difficult so Ellis developed a compiler Bulldog that incorporates a number of optimizations It uses trace scheduling to find more parallelism memory reference and memory bank disambiguation to increase memory bandwidth and a new code generation algorithms

A Compiler for Application-specific Signal Processors Kenneth E. Rimey,1990 I have built a compiler that generates code for a family of application specificdigital signal processors developed at Berkeley by members of the Lager project Application specific processors are programmable processors that serve a components of application specific integrated circuits

The Complexity of Robot Motion Planning John Canny,1988 The Complexity of Robot Motion Planning makes original contributions both to roboticsand to the analysis of algorithms In this groundbreaking monograph John Canny resolveslong standing problems concerning the complexity of motion planning and for the central problem offinding a collision free path for a jointed robot in the presence of obstacles obtains exponential speedups over existing algorithms by applying high powered new mathematical techniques Canny s newalgorithm for this generalized movers problem the most studied and basic robot motion planningproblem has a single exponential running time and is polynomial for any given robot The algorithmhas an optimal running time exponent and is based on the notion of roadmaps one dimensionalsubsets of the robot s configuration space In deriving the single exponential bound Cannyintroduces and reveals the power of two tools that have not been previously used in geometricalgorithms the generalized multivariable resultant for a system of polynomials and Whitney snotion of stratified sets He has also developed a novel representation of object orientation basedon unnormalized quaternions which reduces the complexity of the algorithms and enhances theirpractical applicability After dealing with the movers problem the book next attacks and derivesseveral lower bounds on extensions of the problem finding the shortest path among polyhedralobstacles planning with velocity limits and compliant motion planning with uncertainty Itintroduces a clever technique path encoding that allows a proof of NP hardness for the first twoproblems and then shows that the general form of compliant motion planning a problem that is thefocus of a great deal of recent work in robotics is non deterministic exponential time hard Cannyproves this result using a highly original construction John Canny received his doctorate from MITAnd is an assistant professor in the Computer Science Division at the University of California Berkeley The Complexity of Robot Motion Planning is the winner of the 1987 ACM

DoctoralDissertation Award **Fourth International Conference on Supercomputing and Third World**

Supercomputer Exhibition ,1989 *Proceedings, Fourth International Conference on Supercomputing and Third World Supercomputer Exhibition, Santa Clara Convention Center, Santa Clara, CA, USA, April 30-May 5, 1989: Supercomputing structures & computations* ,1989 **VLSI Fault Modeling and Testing Techniques** George W. Zobrist,1993 VLSI systems are becoming very complex and difficult to test Traditional stuck at fault problems may be inadequate to model possible manufacturing defects in the integrated circuit Hierarchical models are needed that are easy to use at the transistor and functional levels Stuck open faults present severe testing problems in CMOS circuits to overcome testing problems testable designs are utilized Bridging faults are important due to the shrinking geometry of ICs BIST PLA schemes have common features controllability and observability which are enhanced through additional logic and test points Certain circuit topologies are more easily testable than others The amount of reconvergent fan out is a critical factor in determining realistic measures for determining test generation difficulty Test implementation is usually left until after the VLSI data path has been synthesized into a structural description This leads to investigation methodologies for performing design synthesis with test incorporation These topics and more are discussed **Computer Design** ,1987 Proceedings: Supercomputer design, performance evaluation, and performance education ,1987 Proceedings of the ACM SIGPLAN '93 Conference on Programming Language Design and Implementation ,1993 Supercomputing '89 ,1989 **Annual Report** Association for Computing Machinery,1985 **ACM SIGPLAN Notices** ,2004-03 **Bibliographic Guide to Computer Science** ,1987

The Publishers' Trade List Annual ,1995 *Resources in Parallel and Concurrent Systems* ,1991 Computer Systems Organization Parallel architecture *Full Abstraction and Semantic Equivalence* Ketan Mulmuley,1987 Full Abstraction and Semantic Equivalence demonstrates an original theory that uses the same inclusive predicates to show semantic equivalence and to construct fully abstract extensional submodels Previous general techniques for proving semantic equivalence through the construction of inclusive predicates have foundered due to their complexity and the lack of any counter examples In this book Mulmuley has been able to construct a counterexample through diagonalization moreover he has discovered a technique to prove semantic equivalences that has the advantage of being mechanizable This system called Inclusive Predicate Logic IPL can almost automatically prove the existence of most of the inclusive predicates which arise in practice Mulmuley also demonstrates that one can construct a fully abstract algebraic model of typed lambda calculus which is a submodel of the classical model if the classical model is based on complete lattices Beyond this the book shows that theory can extend to the case of a language that has reflexive recursively defined types as do most of the real programming languages Contents Introduction to Domain Theory The Problem of Inclusive Predicate Existence Fully Abstract Submodels of Typed Lambda Calculi Fully Abstract Submodels in the Presence of Reflexive Types A Mechanizable Theory for Existence Proofs IPL Implementation Conclusion Ketan Mulmuley is a Miller Fellow in the Department of Electrical Engineering and Computer Science at the University of California Berkeley Full Abstraction and Semantic Equivalence is a winner of the 1986

ACM Doctoral Dissertation Award **The Design and Analysis of Efficient Learning Algorithms** Robert E. Schapire, 1992 This monograph describes results derived from the mathematically oriented framework of computational learning theory

Bulldog A Compiler For Vliw Architectures Acm Doctoral Dissertation Award 1985: Bestsellers in 2023 The year 2023 has witnessed a noteworthy surge in literary brilliance, with numerous captivating novels captivating the hearts of readers worldwide. Lets delve into the realm of top-selling books, exploring the engaging narratives that have captivated audiences this year. The Must-Read : Colleen Hoovers "It Ends with Us" This poignant tale of love, loss, and resilience has captivated readers with its raw and emotional exploration of domestic abuse. Hoover expertly weaves a story of hope and healing, reminding us that even in the darkest of times, the human spirit can triumph. Bulldog A Compiler For Vliw Architectures Acm Doctoral Dissertation Award 1985 : Taylor Jenkins Reids "The Seven Husbands of Evelyn Hugo" This captivating historical fiction novel unravels the life of Evelyn Hugo, a Hollywood icon who defies expectations and societal norms to pursue her dreams. Reids captivating storytelling and compelling characters transport readers to a bygone era, immersing them in a world of glamour, ambition, and self-discovery. Bulldog A Compiler For Vliw Architectures Acm Doctoral Dissertation Award 1985 : Delia Owens "Where the Crawdads Sing" This evocative coming-of-age story follows Kya Clark, a young woman who grows up alone in the marshes of North Carolina. Owens spins a tale of resilience, survival, and the transformative power of nature, captivating readers with its evocative prose and mesmerizing setting. These top-selling novels represent just a fraction of the literary treasures that have emerged in 2023. Whether you seek tales of romance, adventure, or personal growth, the world of literature offers an abundance of compelling stories waiting to be discovered. The novel begins with Richard Papen, a bright but troubled young man, arriving at Hampden College. Richard is immediately drawn to the group of students who call themselves the Classics Club. The club is led by Henry Winter, a brilliant and charismatic young man. Henry is obsessed with Greek mythology and philosophy, and he quickly draws Richard into his world. The other members of the Classics Club are equally as fascinating. Bunny Corcoran is a wealthy and spoiled young man who is always looking for a good time. Charles Tavis is a quiet and reserved young man who is deeply in love with Henry. Camilla Macaulay is a beautiful and intelligent young woman who is drawn to the power and danger of the Classics Club. The students are all deeply in love with Morrow, and they are willing to do anything to please him. Morrow is a complex and mysterious figure, and he seems to be manipulating the students for his own purposes. As the students become more involved with Morrow, they begin to commit increasingly dangerous acts. The Secret History is a brilliant and thrilling novel that will keep you guessing until the very end. The novel is a warning tale about the dangers of obsession and the power of evil.

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